

HFA25PB60

HEXFRED™

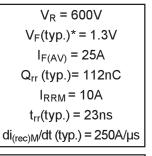
Ultrafast, Soft Recovery Diode

Features

- Ultrafast Recovery
- · Ultrasoft Recovery
- Very Low I_{RRM}
- Very Low Q_{rr}
- · Specified at Operating Conditions

Benefits

- · Reduced RFI and EMI
- Reduced Power Loss in Diode and Switching Transistor
- Higher Frequency Operation
- · Reduced Snubbing
- · Reduced Parts Count





Description

International Rectifier's HFA25PB60 is a state of the art ultra fast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 600 volts and 25 amps continuous current, the HFA25PB60 is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultra fast recovery time, the HEXFRED product line features extremely low values of peak recovery current (I_{RRM}) and does not exhibit any tendency to "snap-off" during the t_b portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED HFA25PB60 is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.

Absolute Maximum Ratings

	Parameter	Max	Units
V _R	Cathode-to-Anode Voltage	600	V
I _F @ T _C = 25°C	Continuous Forward Current		
I _F @ T _C = 100°C	Continuous Forward Current	25	A
I _{FSM}	Single Pulse Forward Current	225	_ ^
I _{FRM}	Maximum Repetitive Forward Current	100	
P _D @ T _C = 25°C	Maximum Power Dissipation	151	١٨/
P _D @ T _C = 100°C	Maximum Power Dissipation	60	W
TJ	Operating Junction and	FF to 14F0	
T _{STG}	Storage Temperature Range	-55 to +150	C

^{* 125°}C



Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min	Тур	Max	Units	Test Conditions	
V_{BR}	Cathode Anode Breakdown Voltage	600			V	I _R = 100μA	
V _{FM}	Max Forward Voltage			1.3	1.7	I _F = 25A	4
			1.5	2.0	V	I _F = 50A See Fig	. 1
			1.3	1.7		I _F = 25A, T _J = 125°C	
I _{RM}	Max Reverse Leakage Current		1.5	20	μA	V _R = V _R Rated See Fig	. 2
			600	2000	μΛ	$T_J = 125$ °C, $V_R = 0.8 \times V_R$ Rated	
C _T	Junction Capacitance		55	100	pF	V _R = 200V See Fig	. 3
L _S	Carias Industanas		12		nH	Measured lead to lead 5mm from	
	Series Inductance					package body	

Dynamic Recovery Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min	Тур	Max	Units	Test Conditions		
t _{rr}	Reverse Recovery Time		23			$I_F = 1.0A$, $di_f/dt = 200A/\mu s$, $V_R = 30V$		
t _{rr1}	See Fig. 5 & 10		50	75	ns	T _J = 25°C		
t _{rr2}			105	160		T _J = 125°C	I _F = 25A	
I _{RRM1}	Peak Recovery Current		4.5	10	Α	T _J = 25°C		
I _{RRM2}	See Fig. 6 & 10		8.0	15	, ,	T _J = 125°C	V _R = 200V	
Q _{rr1}	Reverse Recovery Charge		112	375	nC	T _J = 25°C		
Q _{rr2}	See Fig. 7 & 10		420	1200	110	$T_J = 125$ °C	$di_f/dt = 200A/\mu s$	
di _{(rec)M} /dt1	Peak Rate of Fall of Recovery Current		250		A/µs	T _J = 25°C		
di _{(rec)M} /dt2	During t _b See Fig. 8 & 10		160		-7. μ s	T _J = 125°C		

Thermal - Mechanical Characteristics

	Parameter	Min	Тур	Max	Units
T _{lead} ①	Lead Temperature			300	°C
R _{thJC}	Thermal Resistance, Junction to Case			0.83	
R _{thJA} ②	Thermal Resistance, Junction to Ambient			40	K/W
R _{thCS} ③	Thermal Resistance, Case to Heat Sink		0.25		
VVt	Weight		6.0		g
			0.21		(oz)
	Mounting Torque	6.0		12	Kg-cm
	Woulding Forque	5.0		10	lbf•in

① 0.063 in. from Case (1.6mm) for 10 sec

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② Typical Socket Mount

③ Mounting Surface, Flat, Smooth and Greased

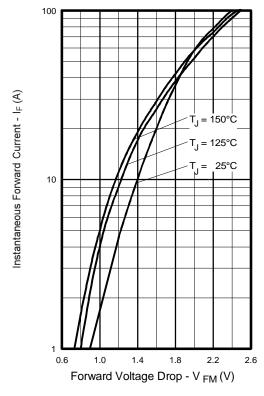


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

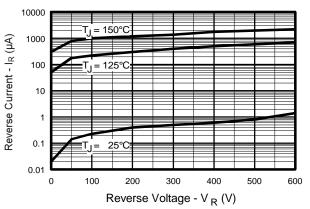


Fig. 2 - Typical Reverse Current vs. Reverse Voltage

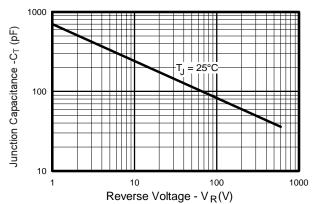


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

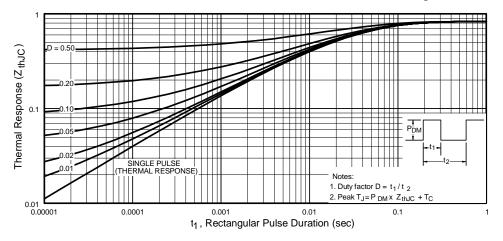


Fig. 4 - Maximum Thermal Impedance Z_{thjc} Characteristics

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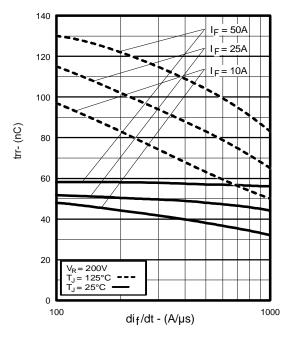


Fig. 5 - Typical Reverse Recovery vs. dif/dt

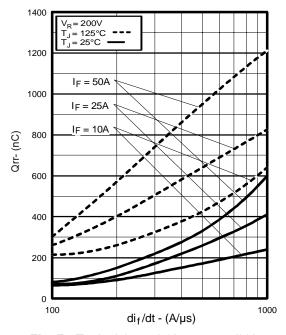


Fig. 7 - Typical Stored Charge vs. dif/dt

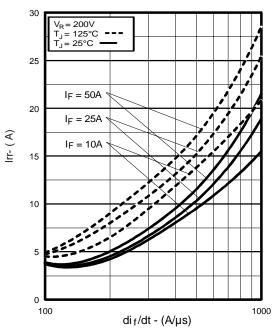


Fig. 6 - Typical Recovery Current vs. di_f/dt

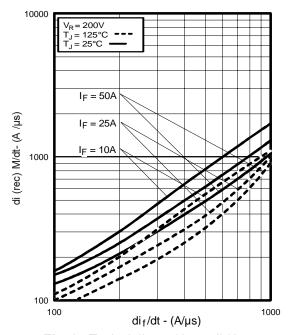


Fig. 8 - Typical di_{(rec)M}/dt vs. di_f/dt

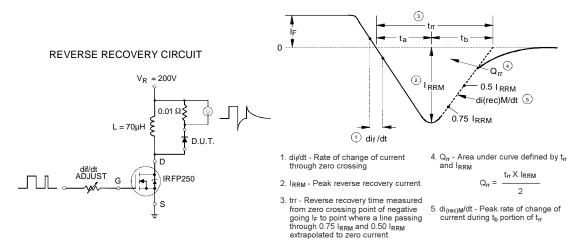
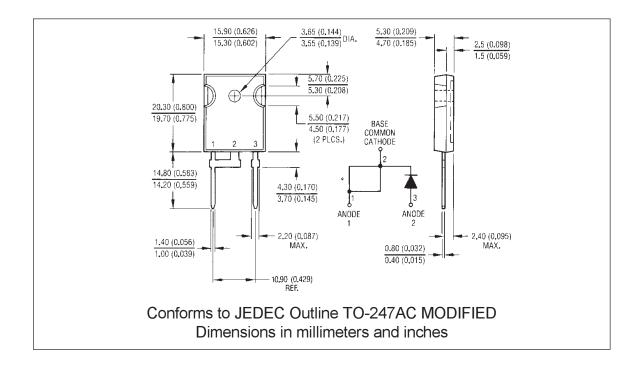


Fig. 9 - Reverse Recovery Parameter Test Circuit

Fig. 10 - Reverse Recovery Waveform and Definitions

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